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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,123	10/31/2003	Chung-Ching Huang	TOP 339	8452

23995 7590 04/04/2006		EXAMINER	
RABIN & Berdo, PC		PATEL, NITIN C	
1101 14TH STREET, NW			
SUITE 500		ART UNIT	PAPER NUMBER
WASHINGTON, DC 20005		2116	

DATE MAILED: 04/04/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

	Application No.	Applicant(s)
Office Action Summary	10/697,123	HUANG ET AL.
	Examiner	Art Unit	
	Nitin C. Patel	2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☐ Responsive to communication(s) filed on _____.
2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.
3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-24* is/are pending in the application.
    4a) Of the above claim(s) _____ is/are withdrawn from consideration.
5)☐ Claim(s) _____ is/are allowed.
6)☒ Claim(s) *1-24* is/are rejected.
7)☐ Claim(s) _____ is/are objected to.
8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.
10)☒ The drawing(s) filed on *31 October 2003* is/are: a)☒ accepted or b)☐ objected to by the Examiner.
    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
    a)☐ All   b)☐ Some * c)☐ None of:
        1.☒ Certified copies of the priority documents have been received.
        2.☐ Certified copies of the priority documents have been received in Application No. _____.
        3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)
1)☒ Notice of References Cited (PTO-892)
2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4)☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5)☐ Notice of Informal Patent Application (PTO-152)
6)☐ Other: _____.

U.S. Patent and Trademark Office
PTOL-326 (Rev. 7-05)   Office Action Summary   Part of Paper No./Mail Date 20060329

DETAILED ACTION

1.   Claims 1 – 24 are presented for the examination.

Specification

2.   The title of the invention is not descriptive. A new title is required that is clearly

indicative of the invention to which the claims are directed. The examiner believes that

the title of the invention is broad. A descriptive title indicative of the invention will help in

proper indexing, classifying, searching, etc. See MPEP 606.01. However, the title of the

invention should be limited to 500 characters.

Claim Objections

3.   Claim 2 is objected to because of the following informalities:

4.   In the claim 2, replace "said computer system" in line 2 on page 8 with ---said

system--- as computer system has not previously recited in the claim.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

        The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5.   Claims 1 – 24 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by

Ahuja, US Patent 6,122,732.

6.   As to claim 1, Ahuja discloses a system of device management [fig. 1],

comprising:

a.   an application device including a RAM (Random-Access Memory) [16,

RAM, fig. 1]; and

b.   a BIOS (Basic Input/output System) ROM (Read-only Memory) [18, Flash

rom] including a BIOS [308, fig. 3] and implementing a specific data structure to store

device information for said application device [col. 1, lines 11 – 16, col. 2, lines 32 – 45,

col. 5, lines 12 – 67, col. 6, lines 1 – 28, fig. 1, 3].

7.   As to claim 13, Ahuja discloses a method of device information management,

comprising:

a.   providing an application device including a RAM (Random-Access

Memory)[16, RAM, fig. 1];

b.   providing a BIOS (Basic Input/output System) ROM (Read-only Memory)

[18, Flash rom] including a BIOS [308, fig. 3]; and

c.   implementing a specific data structure [fig. 3] to said BIOS ROM [300] to

store device information of said application device [col. 1, lines 11 – 16, col. 2, lines 32

– 45, col. 5, lines 12 – 67, col. 6, lines 1 – 28, fig. 1, 3].

8.   As to claims 2 – 3, and 14 – 15, Ahuja teaches a computer system management

and method including when system boots, said BIOS reads device information, and

writes into said RAM and load it from RAM [inherent to the computer system boot-up to

read device information from ROM and copy it to RAM and load it from RAM].

9.   As to claims 4, and 16, Ahuja teaches a computer system management and

method including to update device information in said BIOS ROM using a standard

function call [col. 1, lines 11 – 15, col. 3, lines 2 – 5, col. 6, lines 63 – 67, col. 8, lines 34 – 38].

10.   As to claims 5, and 17, Ahuja teaches a computer system management and method including to update device information in said BIOS ROM using a standard function call by writing updated device information into said RAM [SRAM][col. 7, lines 54 – 67, col. 8, lines 1 – 12, step 512, fig. 5A].

11.   As to claims 6, and 18, Ahuja teaches a computer system management and method including to load updated device information from RAM [col. 7, lines 54 – 67, col. 8, lines 1 – 12, step 512, fig. 5A].

12.   As to claims 7 and 19, Ahuja teaches a computer system management and method including specific data structure as DMI [desktop management interface] data structure [col. 1, lines 11 – 15, col. 2, lines 37 – 38, col. 3, lines 2 – 4, col. 4, lines 41 – 43, lines 62 – 67, col. 5, lines 1 – 3, fig. 1 – 3].

13.   As to claims 8, and 20, Ahuja teaches a computer system management and method including a standard function call which inherently includes a BIOS PNP function call [col. 6, lines 64 – 67].

14.   As to claims 9, and 21, Ahuja teaches a computer system management and method including LAN as an application device [col. 5, lines 4 – 11].

15.   As to claims 10, and 22, Ahuja teaches a computer system management and method including LAN as an application device [col. 5, lines 4 – 11], which inherently teaches an IEEE 1394 chip [col. 5, lines 4 – 11, fig. 2].

16.   As to claims 11, and 23, Ahuja teaches a computer system management and method including a SNMP protocol, which inherently teaches PHY ID as device information [col. 1, lines 62 – 67].

17.   As to claims 12, and 24, Ahuja teaches a computer system management and method including a LAN management application [213], which inherently teaches a MAC address as device information [col. 5, lines 4 – 11, fig. 2].

18.   **Examiner's note**: Examiner has cited particular columns and line numbers in the references as applied to the claims above for the convenience of the applicant. Although the specified citations are representative of the teachings of the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner.

19.   **Prior Art not relied upon**: Please refer to the references listed in attached PTO-892, which, are not relied upon for claim rejection since these references are relevant to the claimed invention.

Conclusion

        Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 571-272-3675. The examiner can normally be reached on 6:45 am - 5:15 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Lynne H. Browne can be reached on 571-272-3670. The fax phone number

for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).
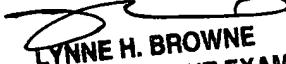
LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Nitin C. Patel
March 30, 2006